

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	216878	(independent\$2 or separat\$2 or (stand adj1 alone)) near3 (process\$2 or task\$1 or operation\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:00
L2	10913	(execut\$3 or run\$4 or complet\$3) near5 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:00
L3	106397	(simultaneous\$2 or concurrent\$2 or (in adj1 parrallel)) near4 (process\$3 or microprocess\$3 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:04
L4	16274	((field adj programmable adj gate adj array) or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:06
L5	10913	1 with 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:06
L6	8	5 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:13
L7	0	5 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:07
L10	46	3 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:23
L11	0	2 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:23
L12	8	2 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:24

L13	184	1 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:24
L14	75	1 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:25
L15	4816	(implement\$3 or design\$3 or us\$3 or hav\$3) near3 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:46
L16	726	15 with (process\$2 or coprocess\$2 or (co adj1 process\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:48
L17	0	16 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:49
L18	0	16 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:49
L19	31	16 same 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:56
L20	251	15 with ((digital adj signal adj process\$3) or DSP\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57
L21	0	20 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57
L22	0	20 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57

L23	3	20 same 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:58
L24	28	15 with ((signal adj distribut\$3 adj matri\$3) or (switch\$3 adj matri\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:04
L25	764	(signal adj2 condition\$3 adj2 filter\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:04
L26	0	15 with 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:05
L27	0	4 with 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:05
L28	1455	(cross adj1 bar adj switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:06
L29	6	15 with 28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:07
L31	181551	((analog-to-digital) or (A adj1 D)) adj conver\$3) or ADC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:20
L32	99605	((digital-to-analog) or (D adj1 A)) adj conver\$3) or DAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:20
L33	48	(31 or 32) with 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:27

L34	510387	(multi adj chip module) or MCM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:30
L35	492	(instrument adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:31
L36	60	(smart adj control adj system\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:31
L37	128	(space adj based) near3 (device\$1 or module\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:32
L38	41370	(application adj specific adj integrated adj circuit) or ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:33
L39	172	(tightly near3 coupl\$3) with 34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:48
L40	1	39 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:34
L41	16	35 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38
L42	1	35 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:35
L43	0	36 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38

L44	1	37 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38
L45	6123	38 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:44
L46	1126	38 same 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:39
L47	924	38 with 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:39
L48	26	47 with 34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:40
L49	0	47 with 39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:40
L50	0	37 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:46
L51	1	37 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:47
L52	1	39 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:48
L53	8281	34 near3 design	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:50

L54	63	53 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:50
L55	35	53 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:58
L56	1658	(discontinuous\$2 or irregular\$2) near4 (power or (power adj operation))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:59
L57	780	((zero adj power) or unpower\$2) near2 (state\$1 or mode\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:00
L58	0	56 with 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:00
L59	0	56 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:01
L60	28645	automatic\$4 near4 (activat\$3 or deactivat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:01
L61	899	(processor\$1 or microprocessor\$1 or CPU\$1) with 60	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:02
L62	92404	(external or outside) near2 signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:03
L63	1	61 with 62	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:04

L64	0	61 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L65	0	61 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L66	0	60 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L67	1	60 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:06
L68	0	37 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:07
L69	0	37 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:07
L70	1	35 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:08
L71	0	35 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:08

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:14
S2	4690	S1 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:55
S3	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S4	10	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S5	739	instrument\$5 adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S6	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S7	2	S5 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:12
S8	56135	microcontroller\$1 or (micro adj controller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S9	1623	S8 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S10	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13



S11	4690	S10 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S12	244720	(processor\$1 or microprocessor\$1 or CPU\$1) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:16
S13	386	S12 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:16
S14	21	S9 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:19
S15	6358	(implement\$3 or (based adj on)) with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:20
S16	1462	((digital adj signal adj process\$3) or DSP\$1) with S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:22
S17	0	S16 same S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:23
S18	169353	((analog adj to adj digital) or (A adj D)) adj convert\$3) or ADC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:27
S19	92482	((digital adj to adj analog) or (D adj A)) adj convert\$3) or DAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:28
S21	26215	(S18 or S19) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:31

S22	44	S21 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34
S23	689	signal adj condition\$3 adj2 filter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34
S24	1	S23 same S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:35
S25	2813	(monitor\$3 or control\$4) near5 S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:36
S26	164	S25 same (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S27	63	S25 with (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S28	22285	motor adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S29	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S30	6358	(implement\$3 or (based adj on)) with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S31	4	S28 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:18

S32	134091	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:25
S33	147347	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:27
S34	2601175	(independent\$2 or autonomous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:32
S35	62	S34 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:30
S36	171069	(process\$2 or task\$1) near3 execut\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S37	3958	S34 with S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S38	0	S37 with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S39	2	S37 same S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:46
S40	9119	coprocessor or (co adj processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:47
S41	32	S40 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:51

S42	526	controller with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:53
S43	17705	(multi adj chip adj module\$1) or MCM\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:54
S44	9	S43 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:56
S45	1	S33 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:59

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	211	(zero adj power\$2) near3 state\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:17
L12	7	(processor\$1 or microprocessor\$1 or CPU\$1) with 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:15
L13	6566	automatic\$2 near4 (activat\$3 or deactivat\$3)	USPAT	OR	OFF	2004/11/22 17:06
L14	187	(processor\$1 or microprocessor\$1 or CPU\$1) with 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:37
L15	56786	(external or outside) adj2 signal\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:25
L16	0	15 with 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:25
L17	13	14 with power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:29
L18	50	external adj activat\$3 adj signal\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:31
L19	324	13 with power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:33
L20	21	19 same (processor\$1 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:35

L22	100	sonar adj2 energy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:05
L24	2	(processor\$1 or microprocessor\$1 or CPU\$1) with 22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:40
L25	3	("5309408").URPN.	USPAT	OR	OFF	2004/11/22 16:38
L26	114	automatic\$3 near4 (wake adj up)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:41
L27	3	26 with processor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:41
L28	40988	(activat\$2 or deactivat\$2) near3 (state\$1 or mode\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:51
L29	1027	28 with (processor\$1 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:50
L30	47	29 with automatic\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:47
L31	20	30 same signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:49
L32	13447	energy near2 conserv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:50
L33	170	32 with (processor\$1 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:50

L34	68	33 with power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:52
L35	4	34 with automatic\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:52
L36	420	unpower\$2 near3 state\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:02
L37	19	(processor\$1 or microprocessor\$1 or CPU\$1) with 36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:07
L38	27392	solar adj2 energy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:05
L39	1	13 with 38	USPAT	OR	OFF	2004/11/22 17:06
L40	75	(processor\$1 or microprocessor\$1 or CPU\$1) with 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:11
L41	20	40 with power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:09
L42	68396	device near3 activat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:16
L43	16	42 with 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 17:16
S1	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:14

S2	4690	S1 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:55
S3	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S4	10	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S5	739	instrument\$5 adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S6	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S7	2	S5 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:12
S8	56135	microcontroller\$1 or (micro adj controller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S9	1623	S8 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S10	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S11	4690	S10 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13



S12	244720	(processor\$1 or microprocessor\$1 or CPU\$1) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 16:17
S13	386	S12 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:16
S14	21	S9 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:19
S15	6358	(implement\$3 or (based adj on)) with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:20
S16	1462	((digital adj signal adj process\$3) or DSP\$1) with S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:22
S17	0	S16 same S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:23
S18	169353	((((analog adj to adj digital) or (A adj D)) adj convert\$3) or ADC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:27
S19	92482	((((digital adj to adj analog) or (D adj A)) adj convert\$3) or DAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:28
S21	26215	(S18 or S19) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:31
S22	44	S21 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34

S23	689	signal adj condition\$3 adj2 filter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34
S24	1	S23 same S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:35
S25	2813	(monitor\$3 or control\$4) near5 S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:36
S26	164	S25 same (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S27	63	S25 with (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S28	22285	motor adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S29	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S30	6358	(implement\$3 or (based adj on)) with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S31	4	S28 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:18
S32	134091	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:25

S33	147347	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:27
S34	2601175	(independent\$2 or autonomous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:32
S35	62	S34 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:30
S36	171069	(process\$2 or task\$1) near3 execut\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S37	3958	S34 with S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S38	0	S37 with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S39	2	S37 same S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:46
S40	9119	coprocessor or (co adj processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:47
S41	32	S40 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:51
S42	526	controller with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:53

S43	17705	(multi adj chip adj module\$1) or MCM\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:54
S44	9	S43 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:56
S45	1	S33 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:59